

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	9864	(distribut\$3 or split) adj (sequenc\$3 or control\$4)	USPAT
2	BRS	L2	3	((distribut\$3 or split) adj (sequenc\$3 or control\$4)) same (FPGA)	USPAT
3	BRS	L3	4	((distribut\$3 or split) adj (sequenc\$3 or control\$4)) same (FPGA or PLD)	USPAT
4	BRS	L4	259	((distribut\$3 or split) adj (sequenc\$3 or control\$4)) same (FPGA or PLD or array)	USPAT
5	BRS	L5	148	((distribut\$3 or split) adj (sequenc\$3 or control\$4)) with (FPGA or PLD or array)	USPAT
6	BRS	L6	134	((distribut\$3) adj (sequenc\$3 or control\$4)) with (FPGA or PLD or array)	USPAT
7	BRS	L7	63	((distribut\$3) adj (sequenc\$3 or control\$4)) with (FPGA or PLD or array) and processor	USPAT
8	BRS	L8	118	(local adj (control\$4 or sequenc\$4)) and FPGA	USPAT
9	BRS	L9	9	(local adj (control\$4 or sequenc\$4)) same FPGA	USPAT
10	BRS	L10	219	(local adj (control\$4 or sequenc\$4)) same (array)	USPAT
11	BRS	L11	207	(local adj (control\$4 or sequenc\$4)) same (array) and \$5process\$4	USPAT
12	BRS	L12	1	"5915123".pn.	USPAT
13	BRS	L13	2	(local adj (sequenc\$4)) same (array) and \$5process\$4	USPAT
14	BRS	L14	2	(local adj (sequenc\$4)) same (array or FPGA OR pld) and \$5process\$4	USPAT
15	BRS	L15	20000	SEQUENCER	USPAT
16	BRS	L16	6417	115 and (FPGA or PLD or array)	USPAT
17	BRS	L17	1525	115 same (FPGA or PLD or array)	USPAT

18	BRS	L18	7	115 same (FPGA or PLD or array) and 712/?.ccls.	USPAT
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	Type	L #	Hits	Search Text	DBs
19	BRS	L19	0	dsitributed adj sequencer	USPAT
20	BRS	L20	71	local near2 sequencer	USPAT
21	BRS	L21	32845 1	l20and (fpga or PLD or array)	USPAT
22	BRS	L22	24	l20 and (fpga or PLD or array)	USPAT

	Type	Hits	Search Text	DBs
1	BRS	1193	cae	USPAT
2	BRS	1193	"cae"	USPAT
3	BRS	20	complex adj arithmetic adj unit	USPAT
4	BRS	29	"4760525".URPN.	USPAT
5	BRS	970	712/10-22.ccls.	USPAT
6	BRS	1	"6092174".PN.	USPAT
7	BRS	1	"5960191".PN.	USPAT
8	BRS	1	"5956518".PN.	USPAT
9	BRS	1	"5603043".PN.	USPAT
10	BRS	1	"5956518".PN.	USPAT
11	BRS	1	"5852740".PN.	USPAT
12	BRS	1	"5892962".pn.	USPAT
13	BRS	1	"6088783".pn.	USPAT
14	BRS	0	removable adj FPGA	USPAT
15	BRS	0	removeable adj FPGA	USPAT
16	BRS	0	removable adj FPGA	USPAT
17	BRS	3	removable near2 arithmetic	USPAT
18	BRS	0	removable near2 provessor	USPAT
19	BRS	164	removable near2 processor	USPAT
20	BRS	28	removable adj processor	USPAT
21	BRS	1	"5355489".pn.	USPAT
22	BRS	1	"5892962".pn.	USPAT
23	BRS	675	data adj router	USPAT
24	BRS	53	(data adj router) and FPGA	USPAT
25	BRS	3	FPGA with (data adj router)	USPAT
26	BRS	5583	FPGA	USPAT
27	BRS	226	FPGA.ti.	USPAT
28	BRS	1	"6230257".pn.	USPAT
29	BRS	38	FPGA and ((data adj router) and (sequenc\$3))	USPAT
30	BRS	172	multiply adj accumulate adj unit	USPAT
31	BRS	0	(multiply adj accumulate adj unit) and pre-adder	USPAT
32	BRS	454	multiply adj accumulate adj operation	USPAT
33	BRS	1131	multiply adj accumulate	USPAT
34	BRS	309	multiply/accumulate	USPAT
35	BRS	111	708/523.ccls.	USPAT
36	BRS	181	712/34.ccls.	USPAT
37	BRS	158	712/35.ccls.	USPAT
38	BRS	211	712/220.ccls.	USPAT
39	BRS	242	712/221.ccls.	USPAT

40	BRS	0	FPGA same sequencer same arithmetic	USPAT
41	BRS	13	FPGA same sequenc\$5 same (arithmetic or ALU)	USPAT